

FIG. 1



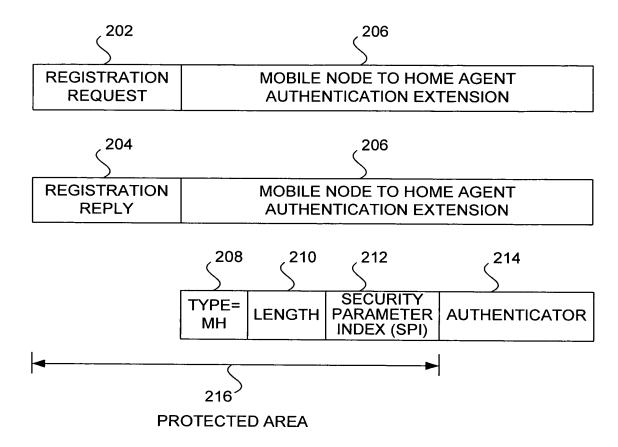


FIG. 2

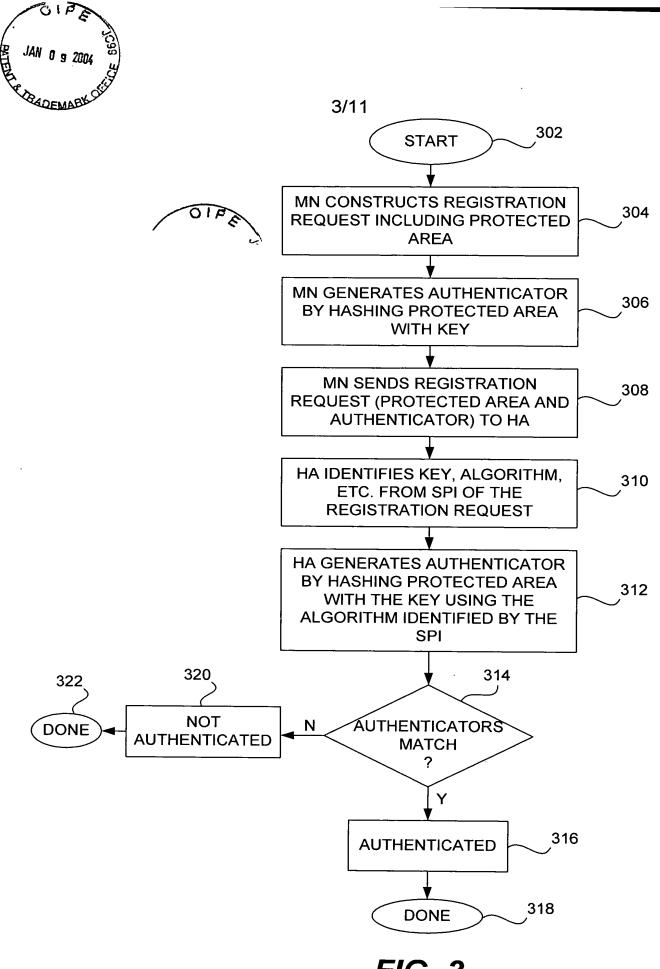


FIG. 3



404 }

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402

406	408	410	412	414	416
MN	SPI	KEY	ALGORITHM	MODE	REPLAY TIMER
IPaddr1	SPI1	key1	e.g., RSA	e.g. prefix- suffix	timestamp1
IPaddr2	SPI2	key2	e.g., RSA	e.g. prefix- suffix	timestamp2
IPaddr3	SPI3	key3	e.g., RSA	e.g. prefix- suffix	timestamp3
:	:	:	:		:
:	• •	••	:	•	:
:	•	• •	:	:	:
IPaddrN	SPIN	keyN	_	_	timestampN

FIG. 4

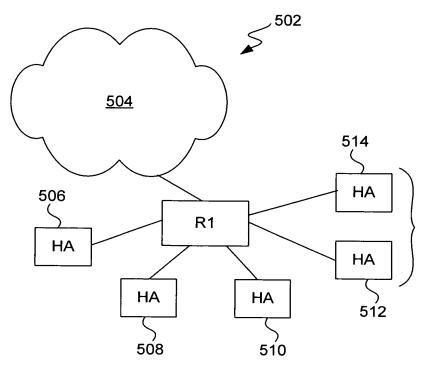


FIG. 5



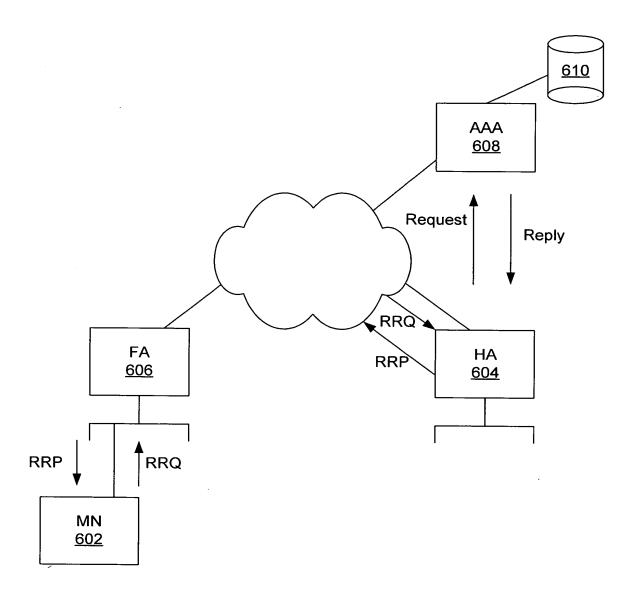
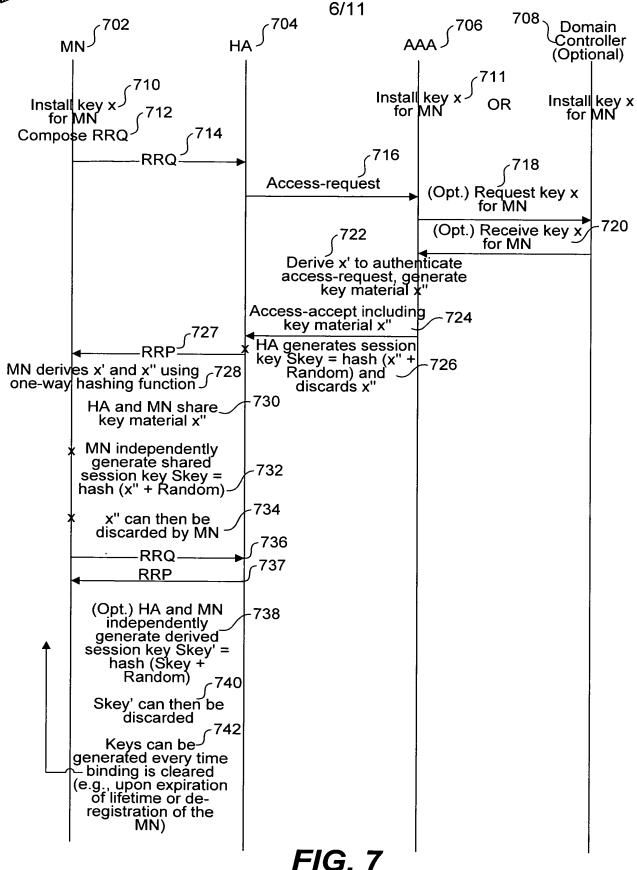
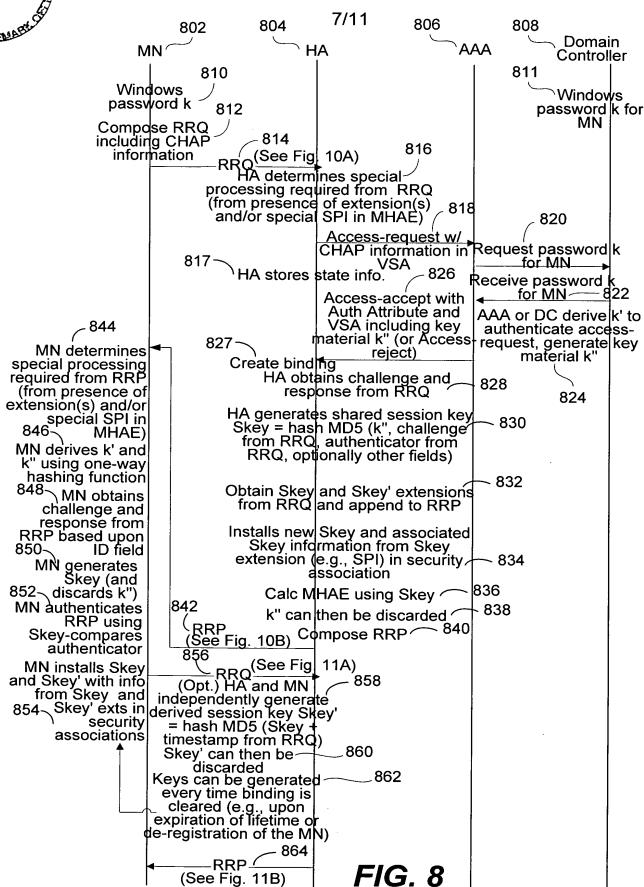


FIG. 6











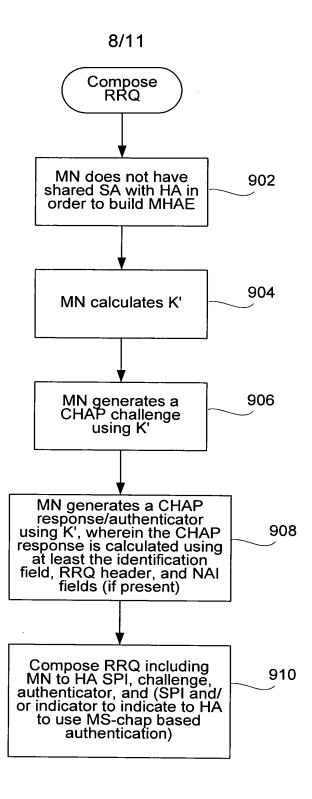


FIG. 9



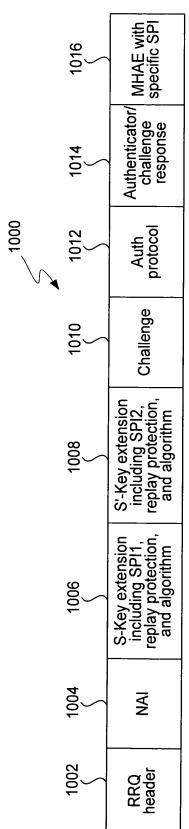


FIG. 10A

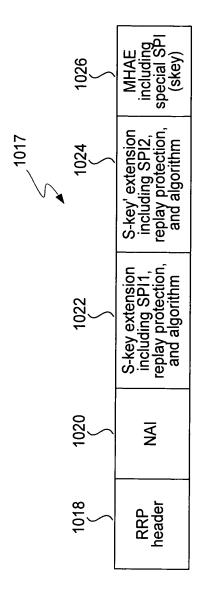


FIG. 10B



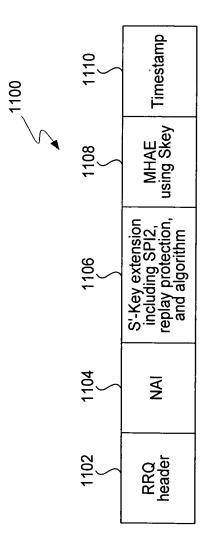


FIG. 11A

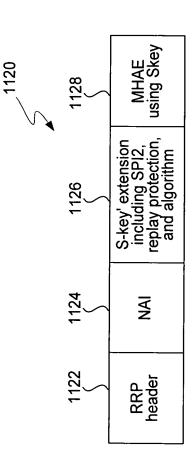


FIG. 11B

